

# EMCal Electronics for MIPP

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**Fermilab**

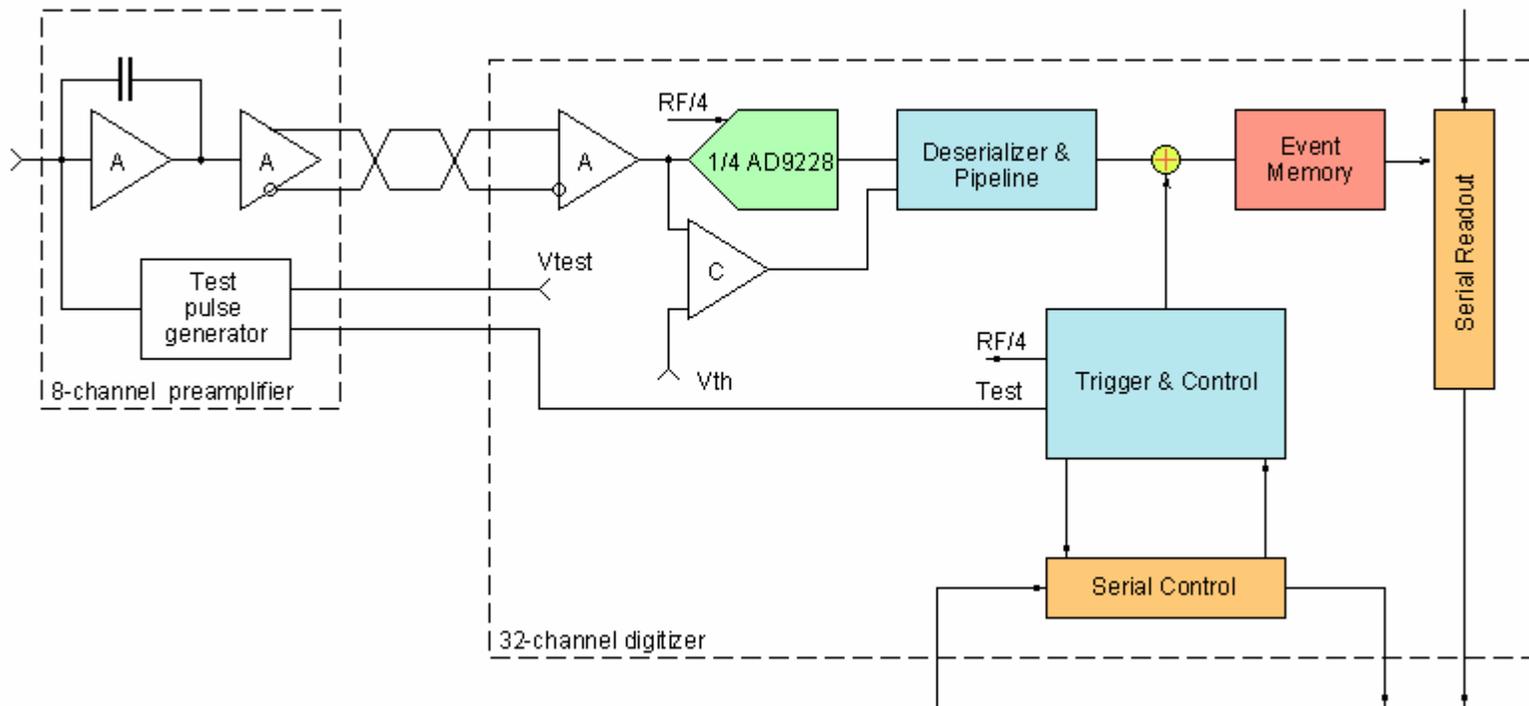
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# General Information

- Total number of channels - 640
- PWC signal range – 0.06..25 pc
- 12 bit ADC to provide 1:100 dynamic range
- Noise level -  $<1e5$
- 8-channel preamplifier board
- Trigger decision time - 2  $\mu\text{s}$
- Minimum interval between triggers - 10  $\mu\text{s}$
- 12,000 events per spill memory storage

# Block Diagram

B. Baldin  
04/28/07



MIPP EMCal Front-end

# Current Status and Plans

- Main parameters of the EMCAL design specification determined
- Use charge measurement in the PFBE design as a basis
- Use the same mechanical standard (6U x 220 mm) as for PFBE
- 16/32-channel EMCAL Readout Card looks feasible
- Design work will start after PFBE prototype is complete